

**NONPROVISIONAL PATENT
APPLICATION TRANSMITTAL RULE §1.53(b)
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

AREN'T FOX KINTNER PLOTKIN & KAHN, PLLC
1050 Connecticut Avenue, NW
Suite 400
Washington, DC 20036-5339
Telephone: (202) 857-6000
Facsimile: (202) 638-4810

Docket Number: 103213-00064

Date: December 1, 2003

Customer Number: **004372**

Mail Stop: Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is a nonprovisional patent application:

For (Title): SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

By (Inventors): Kazuo SATO (Kyoto, Japan)

- ☒ 13 pages of Specification/Claims 1-8/Abstract are attached.
- ☒ Formal drawings (Figs. 1-5; 5 sheets) are attached.
- ☒ Priority of Japanese Application Number 2002-349987 filed December 2, 2002, is claimed under 35 U.S.C. §119.
- ☒ A certified copy of the above corresponding foreign application is attached.
- ☒ An Information Disclosure Statement is attached, with Form PTO-1449, and 1 reference.

The filing fee is calculated below and includes claim status after entry of any Preliminary Amendment noted above:

FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	8 - 20	= 0
INDEP CLAIMS	1 - 3	= 0
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS		

SMALL ENTITY

RATE	FEE
	\$ 385
x 9 =	\$
x 43 =	\$
+145 =	\$
TOTAL	\$

OR

OR

OR

OR

OR

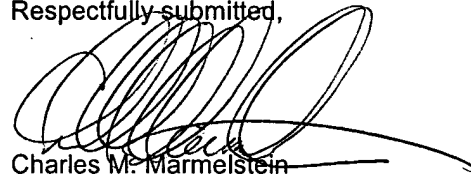
OR

LARGE ENTITY

RATE	FEE
	\$ 770
x 18	\$ 0
x 86	\$ 0
+290	\$ 0
TOTAL	\$ 770

- ☒ **NO FEE IS ENCLOSED AT THIS TIME.**

Respectfully submitted,



Charles M. Marmelstein
Registration Number 25,895

CMM:vmh